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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/138,742 05/03/2002 PAT 6,627,466 *QJ*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

*none, QJ*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/06/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions mat	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY OR	SHEETS DRAWING 2	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
Verified and Acknowledged <i>Examiner's Signature</i>	<i>Initials</i>				

ADDRESS

LSI Logic Corporation

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TITLE

Method and apparatus for detecting backside contamination during fabrication of a semiconductor wafer

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